

FIG. 1 is a block diagram of a system for subpixel mixture analysis. The system includes a contour definition system (40) and a subpixel mixture analysis system (60). The contour definition system (40) receives an input image (20a) and outputs a contour (50). The subpixel mixture analysis system (60) receives the contour (50) and outputs a subpixel mixture analysis result (64).

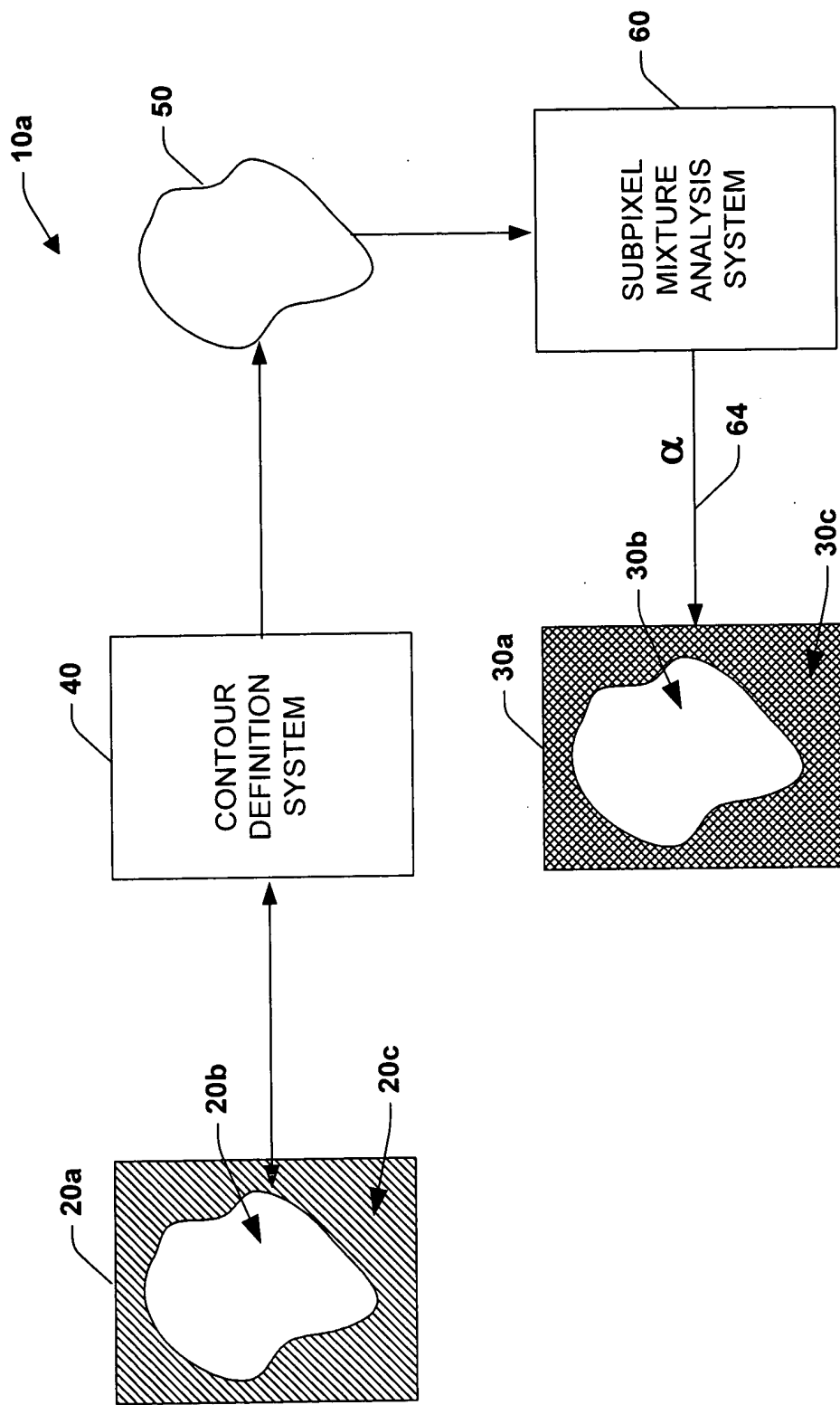


Fig. 1

FIG. 2 is a schematic diagram of a system 50, including a component 70, which is a grid of cells 72a through 72f, and a component 80, which is a circular element with a dashed line and an arrow pointing to the grid.

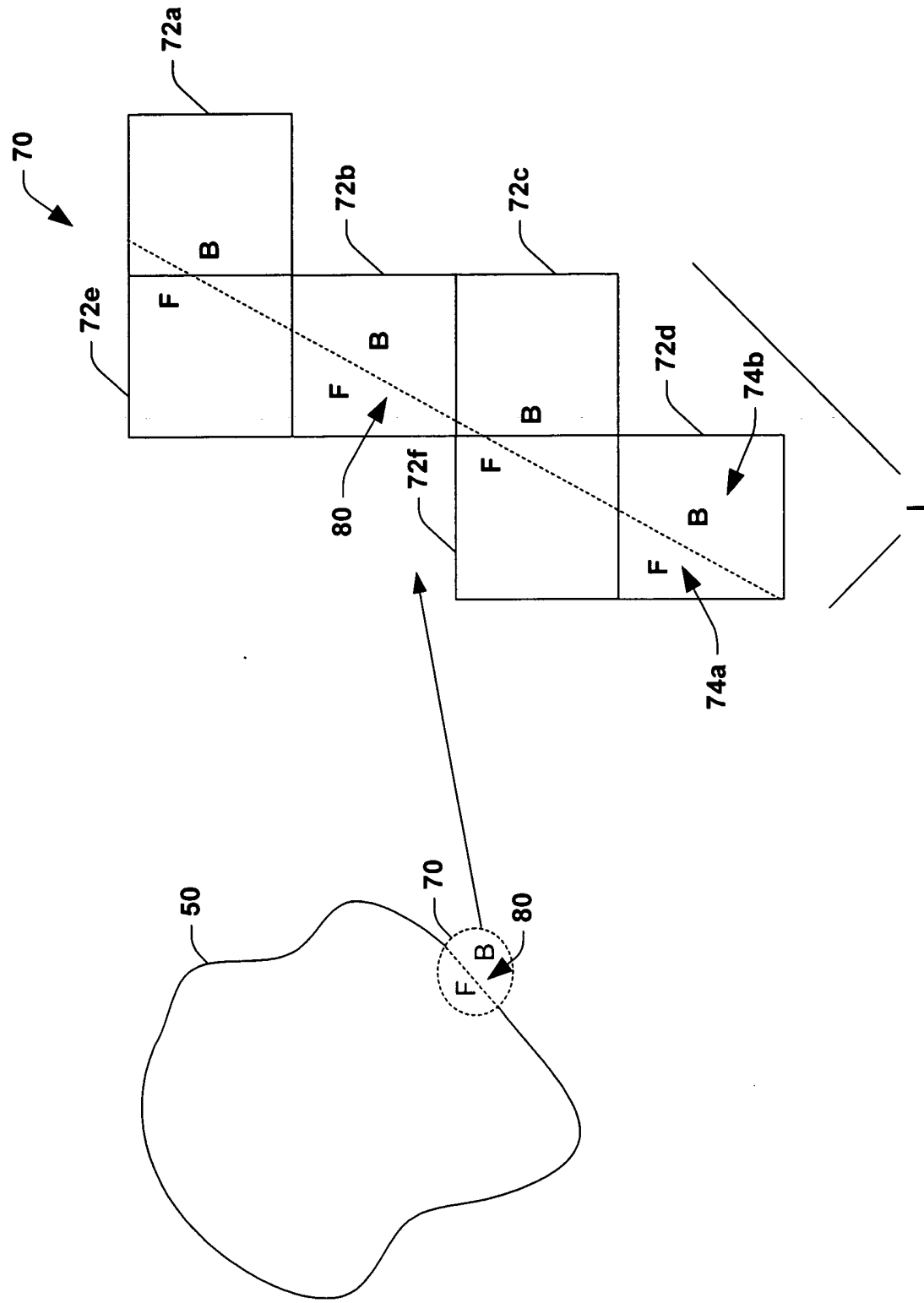


Fig. 2

FIG. 3 is a schematic diagram of a grid of cells 90a through 90h. The grid is defined by X and Y axes. A dashed line 86 is shown in the center cell 90e, and a point 72d is marked on it. A magnified view of the center cell is shown in FIG. 4.

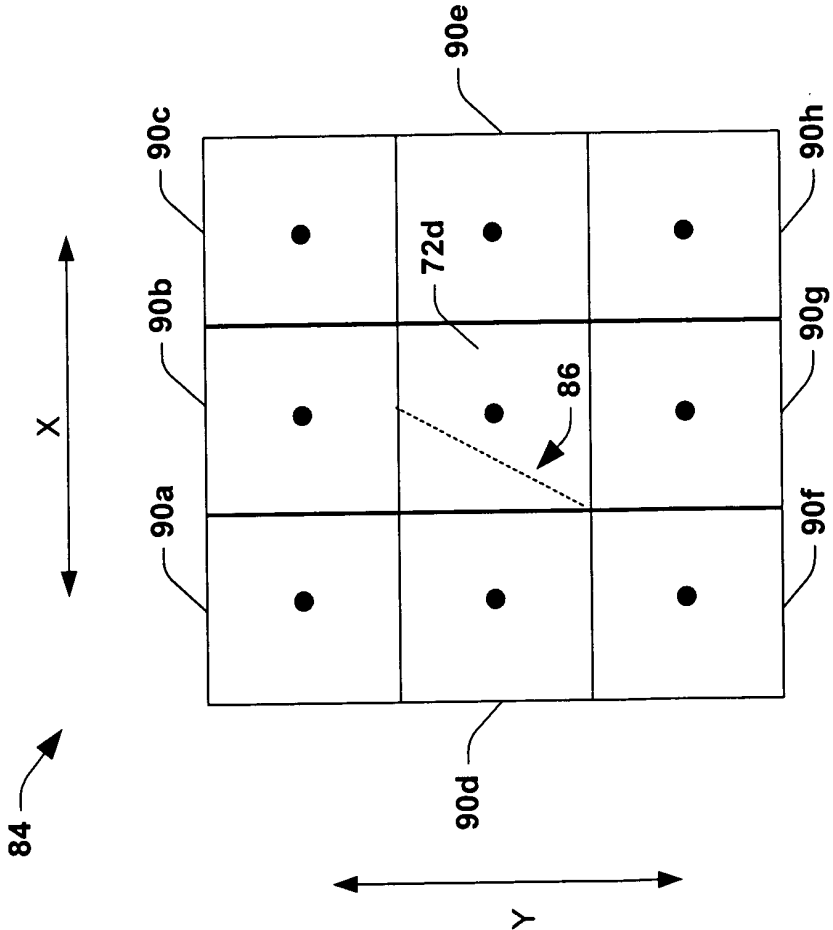


Fig. 3

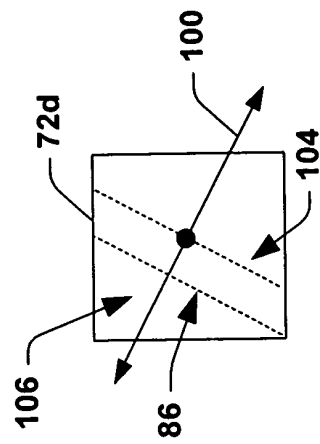


Fig. 4

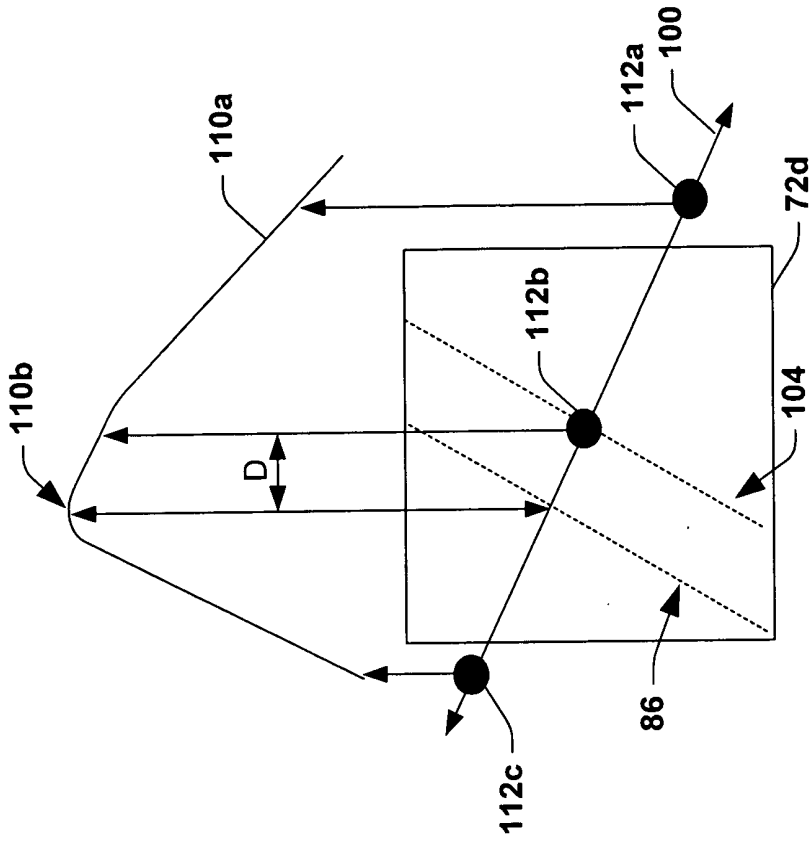


Fig. 5

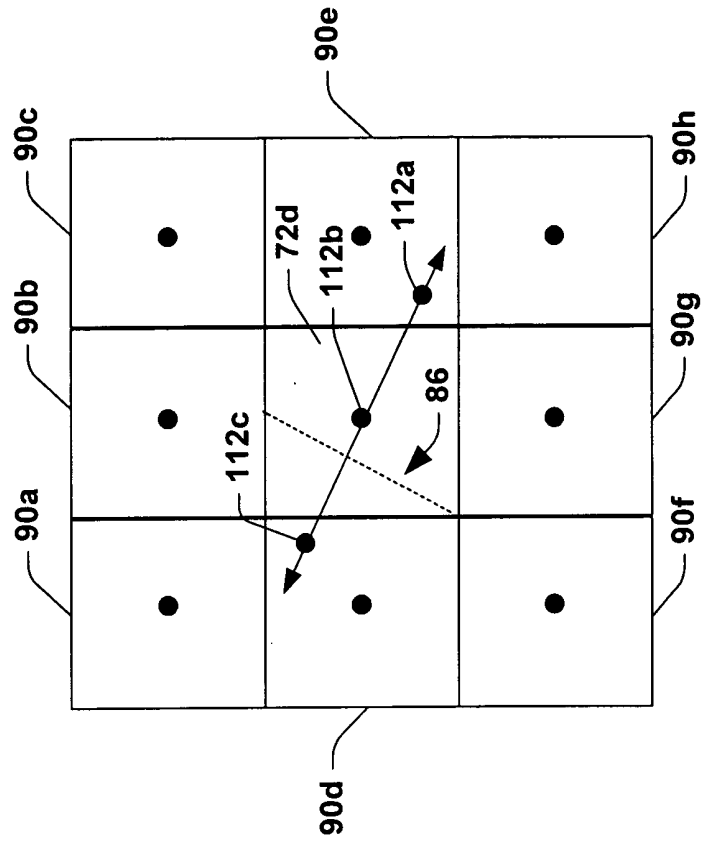


Fig. 6

FIG. 7 is a schematic diagram of a system 100, including a processor 104, a memory 106, and a network interface 108. The processor 104 is connected to the memory 106 and the network interface 108. The network interface 108 is connected to a network 110. The network 110 is connected to a server 112. The server 112 is connected to a database 114. The database 114 is connected to a user interface 116. The user interface 116 is connected to a user 118.

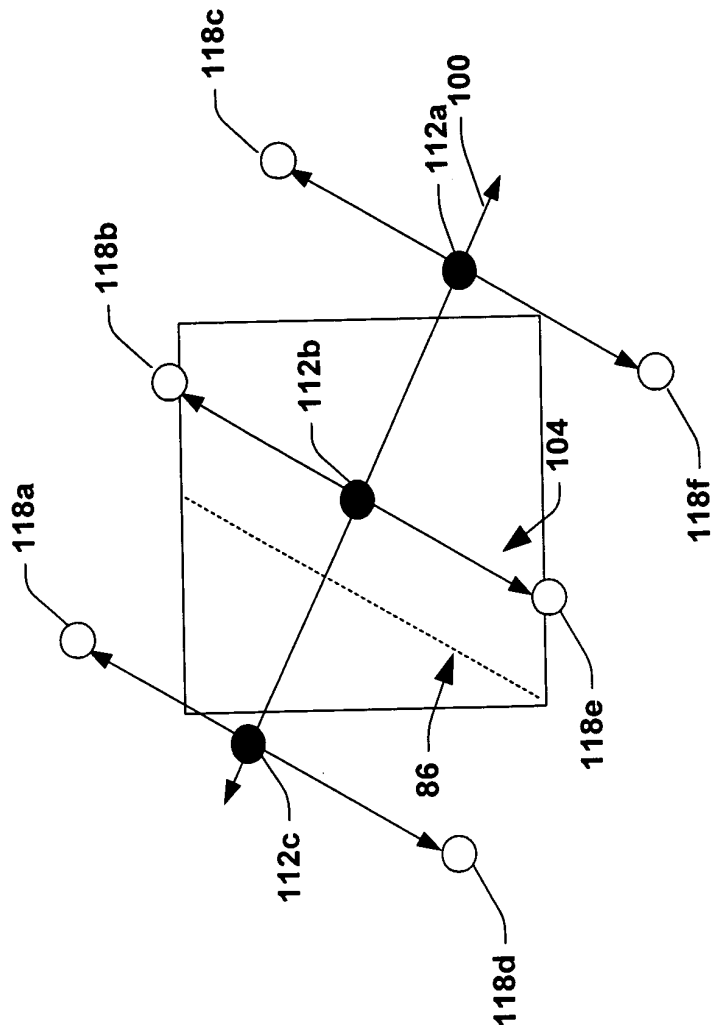


Fig. 7

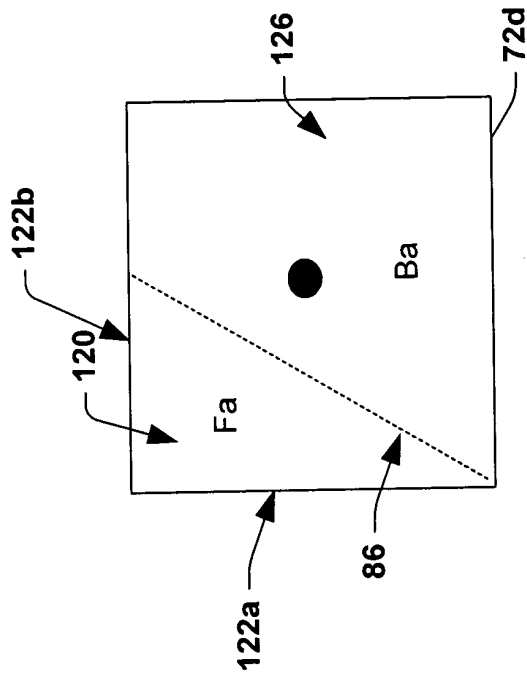


Fig. 8

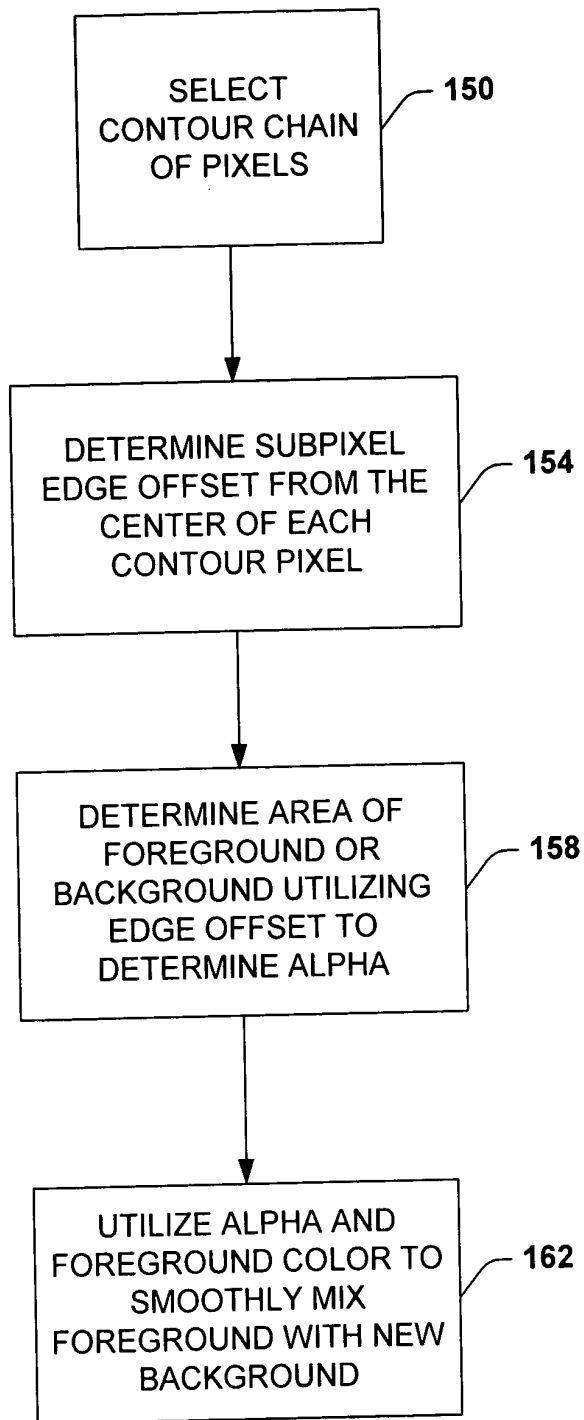


Fig. 9

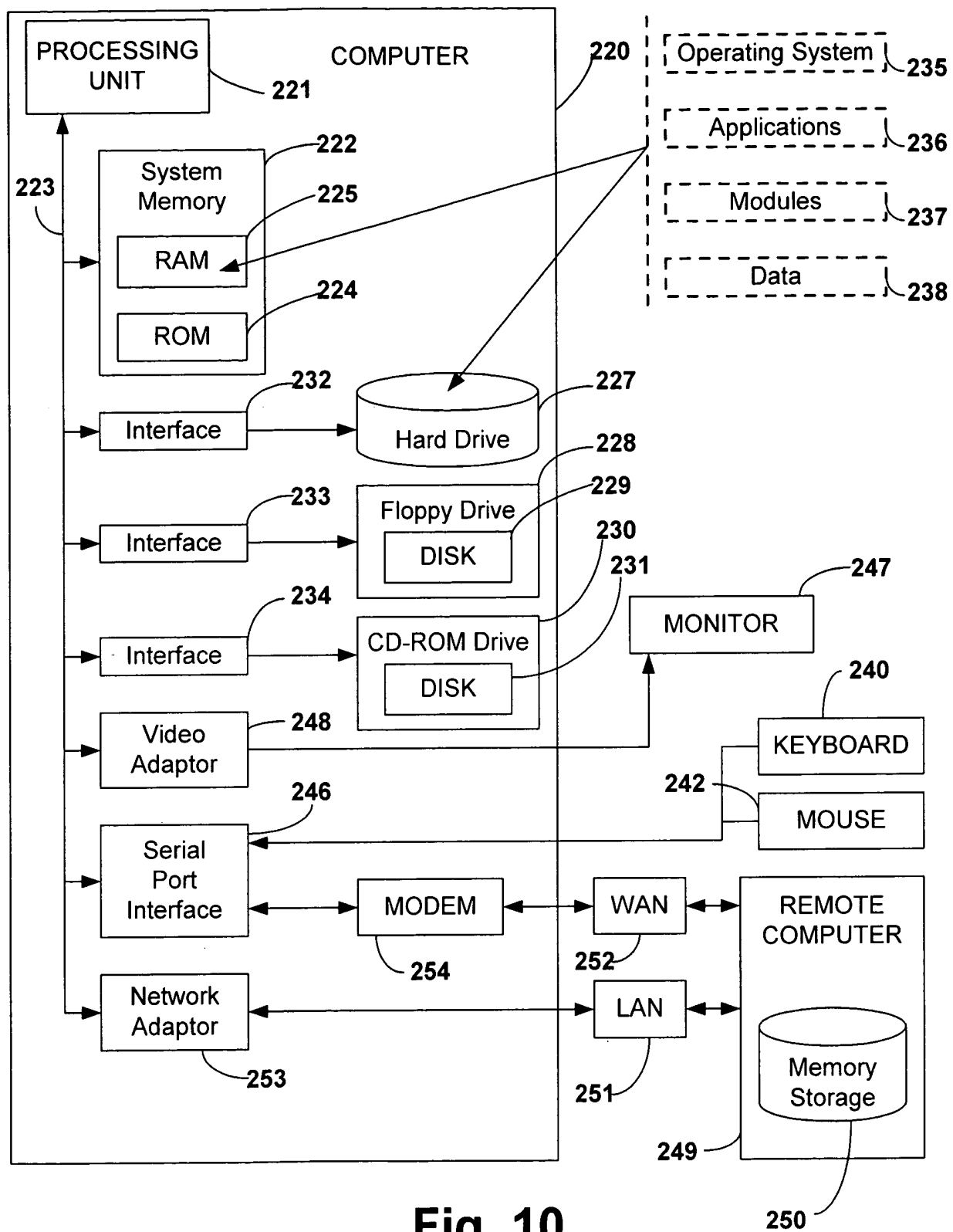


Fig. 10